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CHIPVISION DESIGN SYSTEMS FORMS TECHNICAL ADVISORY BOARD
Low-Power Design Visionaries to Provide Insights and Advise ChipVision Management

OLDENBURG, Germany and SAN JOSE, Calif. – Feb. 9, 2007 – ChipVision Design Systems today announced it has established a Technical Advisory Board (TAB) comprising internationally recognized visionaries from academia and the Electronic Design Automation (EDA) industry. TAB members, who bring a wealth of experience, knowledge and talent, will advise ChipVision executives and engineering management on technology directions for its low-power design solutions and services, and keep the company apprised of academic research in low-power trends. Initial TAB members include Raul Camposano, who previously served as chief technology officer and senior vice president at Synopsys; Jason Cong, professor and chairman of the Computer Science Department of University of California, Los Angeles (UCLA), and a co-director of the VLSI CAD Laboratory; and Jim Hogan, a private venture capital investor who has worked in the semiconductor industry for more than three decades.

“We are honored to have these distinguished and highly accomplished advisors serve on our TAB,” said Thomas Blaesi, chief executive officer at ChipVision. “I view their guidance as invaluable for helping ChipVision significantly improve productivity in companies’ power-sensitive designs – especially for mobile communications, networking, consumer and automotive applications.”

Biography of Raul Camposano

Dr. Raul Camposano holds a B.S.E.E. and a diploma in E.E. from the University of Chile, and a Ph.D. in Computer Science from the University of Karlsruhe. From 2004 to 2006, he served as Senior Vice President, Chief Technology Officer and General Manager, Silicon Engineering Group at Synopsys. Prior to that time, he was Senior VP and CTO from September 2000 to July 2004, and Senior VP, General Manager of the Design Tools Group from 1997 through September 2000. Prior to joining Synopsys in 1994, he directed the Design Technology Institute at the German National Research Center for Computer Science (GMD) and was a professor in the Department of Computer Science at the University of Paderborn, Germany. Between 1986 and 1991, Dr. Camposano worked at the IBM T.J. Watson Research Center. He was also a member of the research staff at the Computer Science Research Laboratory at the University of Karlsruhe. Elected an IEEE Fellow in 1999, he has served on technical program committees and editorial boards worldwide and has published more than 70 articles and three books on EDA.

Biography of Jason Cong

Jason Cong received a B.S. degree in computer science from Peking University, and M.S. and Ph. D. degrees in computer science from the University of Illinois at Urbana-Champaign. Currently, he is a professor and the chairman of the Computer Science Department of UCLA, and a co-director of the VLSI CAD Laboratory. His research interests include computer-aided design of VLSI circuits and systems, design and synthesis of system-on-a-chip, programmable systems, novel computer architectures, nano-systems, and highly scalable algorithms. Dr. Cong has published more than 230 research papers, led more than 30 research projects; served on the technical program and executive committees of many conferences; served on multiple editorial boards, and has been a guest professor at Peking University since 2000. He holds numerous awards and recognitions. Elected an IEEE Fellow in 2000, Dr. Cong has served on the Technical Advisory Boards of Atrenta, eASIC, Get2Chip, Magma Design Automation, and Ultima Interconnect Technologies, and was the founder and president of Aplus Design Technologies, Inc., until its acquisition by Magma Design Automation. Currently, he serves as Chief Technology Advisor of Magma and AutoESL Design Technologies, Inc.

Biography of Jim Hogan

Jim Hogan holds a B.A. degree in mathematics, a B.S. degree in computer science and an M.B.A., all from San Jose State University. Now a private venture capital investor, he has worked in the semiconductor industry for more than three decades as a senior executive in electronic design automation, semiconductor intellectual property, semiconductor equipment, and fabrication companies. Prior to joining Telos Venture Partners in 2004, Jim was Senior Vice President of Business Development of Artisan Components. Previously, he was Senior Vice President of Business Development and the senior member of the Office of Chief Technologist at Cadence Design Systems. He also was a Cadence Executive Fellow and held several positions at Cadence including President of Cadence Japan, Corporate Vice President of Marketing, and Corporate Vice President for Field Operations. Prior to Cadence, he served as Chief Operating Officer of Smart Machines, Inc., a semiconductor equipment automation company. Earlier, he worked for National Semiconductor for 10 years and for Philips, establishing device physics laboratories globally for both companies. He serves on several private boards.

About ChipVision Design Systems

ChipVision Design Systems is the leading supplier of low power system-level electronic design automation (EDA) software tools and services. ChipVision's ORINOCO® software enables semiconductor developers to estimate and optimize the energy dissipation in critical blocks of their design prior to RTL development. The impact of this electronic system-level (ESL) approach is significant energy savings in less time. The company's solutions are based on open industry standards including SystemC®. ChipVision is headquartered in Oldenburg, Germany, and has offices in Munich and San Jose, Calif. For more information about ChipVision and how its products and services can increase productivity in power-sensitive designs, please visit www.chipvision.com or call +49 441 35042 400 in Europe or +1 408 449 4550 in the U.S.

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